

**Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only**

EFS ID: 67502
Application ID: 10711137 
Title of Invention: Embedded Processor with
Watchdog Timer for
Programmable Logic
First Named Inventor: Andrew Crosland
Domestic/Foreign Application: Domestic Application
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FEE RECORD SHEET

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01 FC:1001 770.00 DA
02 FC:1201 86.00 DA
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